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V Reddy, S Swanson, B Seigelke, KA Kantardjieff, JC ... - *Acta Crystallographica Section D: Biological Crystallography*, 2003 - dx.doi.org

... electron-density map improvement and structure validation on a Linux multi-CPU web cluster: The TB Structural Genomics Consortium Bias Removal Web Service. ...

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Computer architecture incorporating processor clusters and hierarchical cache memories

MA Blake, CB Ford III, P Mak - US Patent 5,752,264, 1998 - Google Patents

... US005752264A [ii] Patent Number: [45] Date of Patent: 5,752,264 May 12, 1998 [54]

COMPUTER ARCHITECTURE INCORPORATING PROCESSOR CLUSTERS AND HIERARCHICAL CACHE ...

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System upgrade and processor service

R Bealkowski, S Dhawan, KC Hinz, PM Thomsen - US Patent 6,378,027, 2002 - Google Patents

... system of claim 9 wherein: said processor array includes a plurality of processor clusters; and said program instructions further quiesce, remove from said ...

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TNet: A reliable system area network- ► [switzerland.net](#) [pdf]

RW Horst, TC Inc, CA Cupertino - IEEE Micro, 1995 - ieeexplore.ieee.org

... Figure 2. CPU cluster connected with the TNet system area ... If there is no way to remove these stale ... The service processor can modify routing tables to configure ...

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Partitioning strategies for structured multiblock grids

J Rantakokko - Parallel Computing, 2000 - Elsevier

... Thus, finding min i (p * i / (p i - 1)) and removing a processor from this cluster increases the load imbalance Δ as little as possible. ...

Cited by 22 - Related articles - Web Search - All 3 versions

Human-mouse alignments with BLASTZ- ► [cshlp.org](#)

S Schwartz, WJ Kent, A Smit, Z Zhang, R Baertsch, ... - *Genome Research*, 2003 - genome.cshlp.org

... We now remove recent repeat elements, run BLASTZ, then adjust ... software available on the 1024 CPU cluster that we use doesn't support processor affinity of ...

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[PDF] ► Predicting the cost and benefit of adapting data parallel applications in clusters

JB Weissman - *Journal of Parallel and Distributed Computing*, 2002 - www-users.cs.umn.edu

... on SPMD data parallel applications in shared clusters, we implemented a ... non-block, migrate-stream, migrate-ghost, add-processor, remove-processor, and dynamic ...

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The DASH prototype: Implementation and performance- ► [psu.edu](#) [pdf]

D Lenoski, J Laudon, T Joe, D Nakahira, L Stevens, ... - *Computer Architecture*, 1992. Proceedings., The 19th Annual ... 1992 - ieeexplore.ieee.org

... directory storage (that would need to track individual **processor** caches instead of **clusters**), the percent ... such a system is that it would **remove** all of ...

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[PDF] ► Load balancing and unbalancing for power and performance in cluster-based systems

E Pinheiro, R Bianchini, EV Carrera, T Heath - Workshop on Compilers and Operating Systems for Low Power, 2001 - edpin.com

... our algorithm are conservative and only **remove** or add ... To avoid excessive overheads, internal **cluster** communication is ... mas- ter node about their CPU, disk, and ...

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Xylem: an operating system for the cedar multiprocessor

P Emrath - IEEE software, 1985 - ieeexplore.ieee.org

... EF1... ____ Global network ____ Global | ; Local network ; | .M|| Local network ;

Ig **Processor cluster** || **Processor cluster** | L .IL ____ - - - - - ...

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